

REMARKS

This is in response to the Final Office Action mailed February 6, 2007.

Claims 1 through 3 are currently pending in the application.

Claims 1 through 3 stand rejected.

Applicant has amended no claims, and respectfully requests reconsideration of the application as amended herein.

PRIORITY

Applicant asserts that the specification numbered paragraph [0001] correctly sets forth the application priority claim of the present application under 35 U.S.C. § 120. Applicant asserts that the United States Patent and Trademark Office has failed to include the continuing divisional application data from the UTILITY PATENT APPLICATION TRANSMITTAL form PTO/SB/05.), which complies with 37 CFR § 1.76, used to file U.S. Patent Application Serial Number 09/133,336, filed August 13, 1998, now U.S. Patent 6,147,316.

Applicant attaches to this amendment a copy of a Certificate of Correction, filed November 8, 2006, for U.S. Patent Application Serial Number 09/133,336, filed August 13, 1998, now U.S. Patent 6,147,316, clearly setting forth that such application is a divisional application of U.S. Patent 5,807,762, issued September 15, 1998. As such, Applicant asserts that the effective filing date of this application is January 17, 1997.

In a telephone conversation with the United States Patent and Trademark Office on March 6, 2007, Applicant was informed by the Certificate of Correction Branch that a Certificate of Correction for U.S. Patent Application Serial Number 09/133,336, filed August 13, 1998, now U.S. Patent 6,147,316 had been granted stating that such application is a divisional application of U.S. Patent 5,807,762, issued September 15, 1998. Attached as Exhibit A to this amendment is a copy of the Transaction History for U.S. Patent Application Serial Number 09/133,336, filed August 13, 1998, now U.S. Patent 6,147,316, showing the Post Issue Communication - Certificate of Correction.

35 U.S.C. § 103(a) Obviousness Rejections

Obviousness Rejection Based on Yabe (U.S. Patent 5,726,074) in view of Di Zenzo et al. (U.S. Patent 6,130,442)

Claims 1 through 3 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Yabe (U.S. Patent 5,726,074) in view of Di Zenzo et al. (U.S. Patent 6,130,442). Applicant respectfully traverses this rejection, as hereinafter set forth.

Applicant further submits that to establish a *prima facie* case of obviousness under 35 U.S.C. § 103 three basic criteria must be met. First, there must be some suggestion or motivation, either in the references themselves or in the knowledge generally available to one of ordinary skill in the art, to modify the reference or to combine reference teachings. Second, there must be a reasonable expectation of success. Third, the cited prior art reference must teach or suggest all of the claim limitations. Furthermore, the suggestion to make the claimed combination and the reasonable expectation of success must both be found in the prior art, and not based on Applicant's disclosure.

Turning to the cited prior art, the Yabe reference teaches or suggests the use of a bar code applied to a wafer to store information regarding the plurality of semiconductor integrated circuits formed on the wafer.

Applicant asserts that the DiZenzo et al. reference is not prior art for the reasons set forth above regarding the claimed invention and cannot be used in any rejection of the claimed inventions.

Applicant asserts that the Yabe reference does not and cannot establish a *prima facie* case of obviousness under 35 U.S.C. § 103 regarding the claimed inventions of independent claims 1, 2, and 3 because such cited prior art fails to teach or suggest all the claim limitations of the claimed inventions. Applicant asserts that the Yabe reference does not teach or suggest the claim limitations of independent claims 1, 2, and 3 calling for "storing an enhanced reliability testing flag in the integrated circuit device associated with a unique identification code of each integrated circuit device of the plurality of integrated circuit devices for indicating whether each integrated circuit device requires enhanced reliability testing" and "storing a reliability testing flag in the integrated circuit device associated with a unique identification code of each

integrated circuit device of the plurality of integrated circuit devices for indicating whether each integrated circuit device requires further reliability testing". In contrast to the claimed inventions of presently amended independent claims 1, 2, and 3 of the present application, the Yabe reference stores information on the wafer using a bar code regarding the speed of the semiconductor integrated circuit using bard code applied by a labeling machine 9B on the wafer, not the requirement of needing enhanced reliability testing stored in the integrated circuit device associated with a unique identification code of each integrated circuit device. Applicant asserts that a bar code label installed on a wafer is not a code in an integrated circuit device as such in the claimed inventions of presently amended independent claims 1, 2, and 3. Therefore, such claims are allowable.

Applicant requests entry of this amendment for the following reasons:

The amendment is timely filed.

The amendment places the application in condition for allowance.

The amendment does not require any further search or consideration.

Applicant submits that claims 1 through 3 are clearly allowable over the cited prior art.

Applicant requests the entry of this amendment, the allowance of claims 1 through 3, and the case passed for issue.

Respectfully submitted,



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Telephone: 801-532-1922

Date: March 22, 2007
JRD/mah:lmh

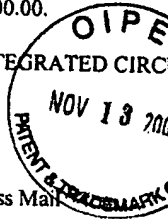
ATTACHEMENTS

Copy of a Certificate of Correction, filed November 8, 2006, for U.S. Patent Application Serial
Number 09/133,336, filed August 13, 1998, now U.S. Patent 6,147,316

PATENT & TRADEMARK OFFICE MAILROOM. DATE
STAMPED HEREON IS AN ACKNOWLEDGEMENT THAT ON
DATE THE PATENT & TRADEMARK OFFICE RECEIVED:

Request for Certificate of Correction of Patent for Applicant(s) Mist
(37 C.F.R. § 1.323) and Patent Office Mistakes (37 C.F.R. § 1.322)
certificate of first class mailing; Certificate of Correction and duplic
copy; and Check No. 23280 in the amount of \$100.00.

Invention: METHOD FOR SORTING INTEGRATED CIRCUIT
DEVICES
Inventor(s): Raymond J. Beffa
Issue Date: November 14, 2000
Patent No.: 6,147,316
Date Sent: November 8, 2006 via First Class Mail
Docket No.: 2269-3037.2US
JRD/csw



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2269-3037.2US

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231

A request for a Certificate of Correction
been received for U.S. Patent 61473

RECEIVED

DEC 04 2006
TRASKBRITT, P.C.
JAMES R. DUZAN
TRASK/BRITT
POST OFFICE BOX 2550
SALT LAKE CITY, UTAH



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Raymond J. Beffa

Patent No.: 6,147,316

Issued: November 14, 2000

For: METHOD FOR SORTING
INTEGRATED CIRCUIT DEVICES

Attorney Docket No.: 2269-3037.2US

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

November 8, 2006

Date

Signature

Leta M. Howard

Name (Type/Print)

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT
FOR APPLICANT'S MISTAKES (37 C.F.R. § 1.323) AND
PATENT OFFICE MISTAKES (37 C.F.R. § 1.322)**

Attn.: Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

It is noted that a combination of Applicant and Patent Office errors appear in this patent of a typographical nature or character and correction thereof does not involve such changes in the patent as would constitute new matter or would require reexamination. A certificate of correction in the form attached hereto is requested.

Please send the Certificate to:

Name: James R. Duzan
Address: TraskBritt
P.O. Box 2550
Salt Lake City, Utah 84110

The fee of \$100.00 as required by 37 C.F.R. § 1.20(a) is enclosed.

Patent No.: 6,147,316

Attached hereto in duplicate is Form PTO/SB/44 with at least one copy being suitable for printing.

Respectfully submitted,



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TRASKBRITT
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Telephone: 801-532-1922

Date: November 8, 2006
JRD/csw

Enclosures: PTO/SB/44 in duplicate
Check No. 23280 in the amount of \$100.00

Document in ProLaw

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO : 6,147,316
DATED : November 14, 2000
INVENTOR(S) : Raymond J. Beffa

Page 1 of 1

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page:

After section [58], insert:

--Related U.S. Application Data

[63] Divisional of application No. 08/785,353, filed on
January 17, 1997, now Pat. No. 5,927,512.--

In the specification:

COLUMN 1,	LINE 6,	after "to:" insert --a divisional and--
COLUMN 3,	LINE 52,	change "first," to --first IC standard,--
COLUMN 7,	LINE 57,	change "6.1," to --6.1 ns,--

In the claims:

CLAIM 1,	COLUMN 8,	LINE 31,	change "of the type" to --of a type--
CLAIM 2,	COLUMN 8,	LINE 50,	change "of the type" to --of a type--

MAILING ADDRESS OF SENDER:

PATENT NO. 6,147,316

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Salt Lake City, Utah 84102 USA

No. of additional copies

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS.
SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

EXHIBIT A

Copy of the Transaction History for U.S. Patent Application Serial Number 09/133,336, filed August 13, 1998, now U.S. Patent 6,147,316, showing the Post Issue Communication - Certificate of Correction

Search results as of: 03-12-2007::15:53:29 E.T.

Transaction History	
Date	Transaction Description
02-27-2007	Post Issue Communication - Certificate of Correction
05-06-2002	Post Issue Communication - Certificate of Correction Denied
08-25-2000	Workflow - File Sent to Contractor
11-14-2000	Recordation of Patent Grant Mailed
10-27-2000	Issue Notification Mailed
10-08-2000	Application Is Considered Ready for Issue
09-18-2000	Issue Fee Payment Verified
10-04-2000	Workflow - Complete WF Records for Drawings
09-18-2000	Workflow - Drawings Finished
09-18-2000	Workflow - Drawings Matched with File at Contractor
09-18-2000	Workflow - Drawings Received at Contractor
09-18-2000	Workflow - Drawings Sent to Contractor
07-06-2000	Mail Notice of Allowance
07-06-2000	Notice of Allowance Data Verification Completed
05-01-2000	Date Forwarded to Examiner
04-24-2000	Response after Non-Final Action
03-01-2000	Mail Non-Final Rejection
02-28-2000	Non-Final Rejection
08-05-1999	Information Disclosure Statement (IDS) Filed
05-26-1999	Case Docketed to Examiner in GAU
03-03-1999	Information Disclosure Statement (IDS) Filed
10-19-1998	Information Disclosure Statement (IDS) Filed
03-19-1999	Case Docketed to Examiner in GAU
01-25-1999	Transfer Inquiry
08-13-1998	Information Disclosure Statement (IDS) Filed
09-04-1998	Application Dispatched from OIPE
08-21-1998	IFW Scan & PACR Auto Security Review
08-18-1998	Initial Exam Team nn

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